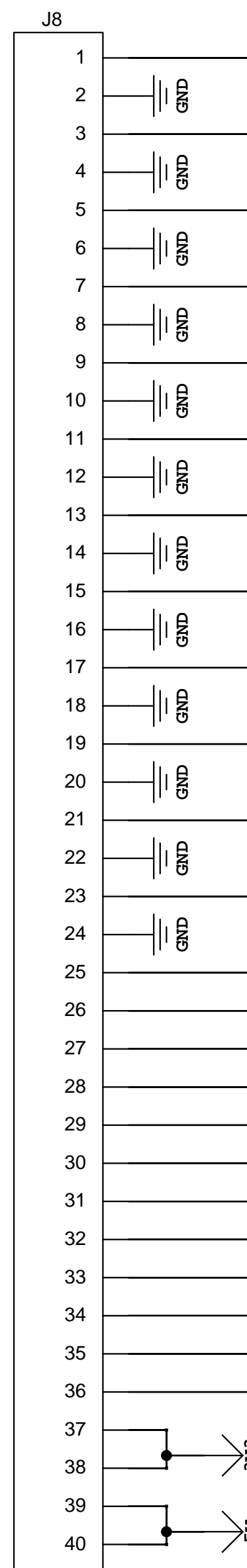
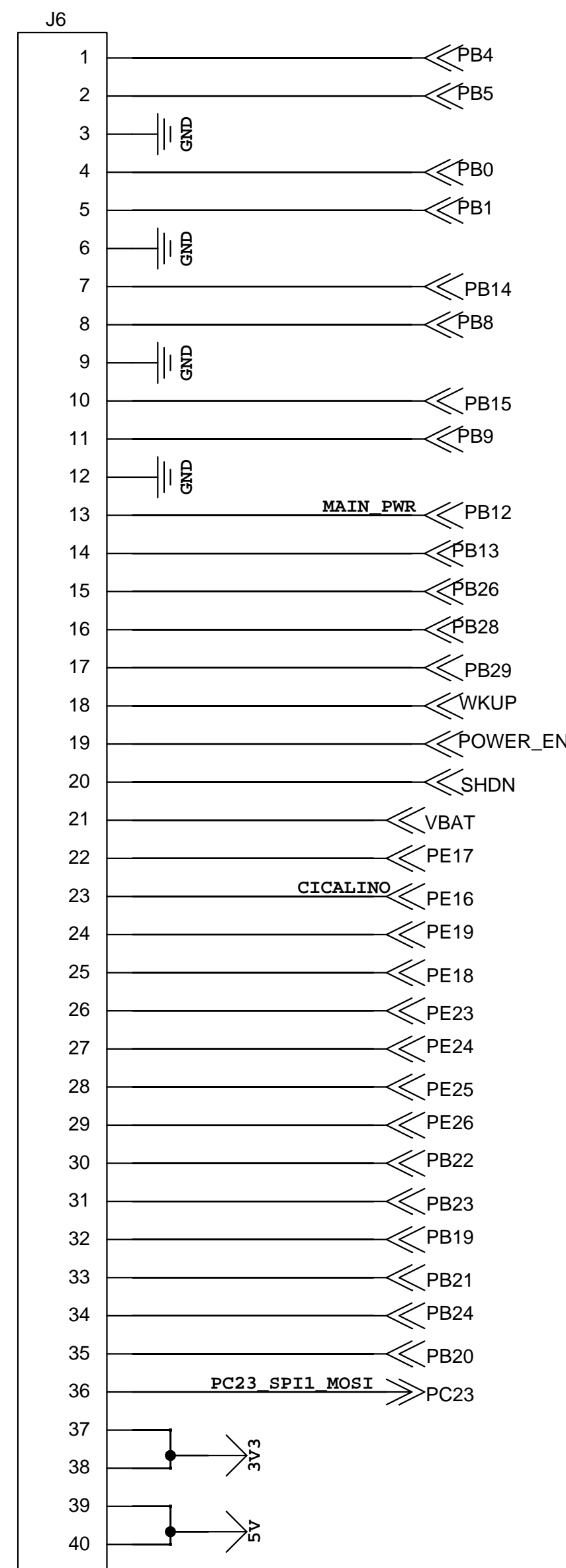


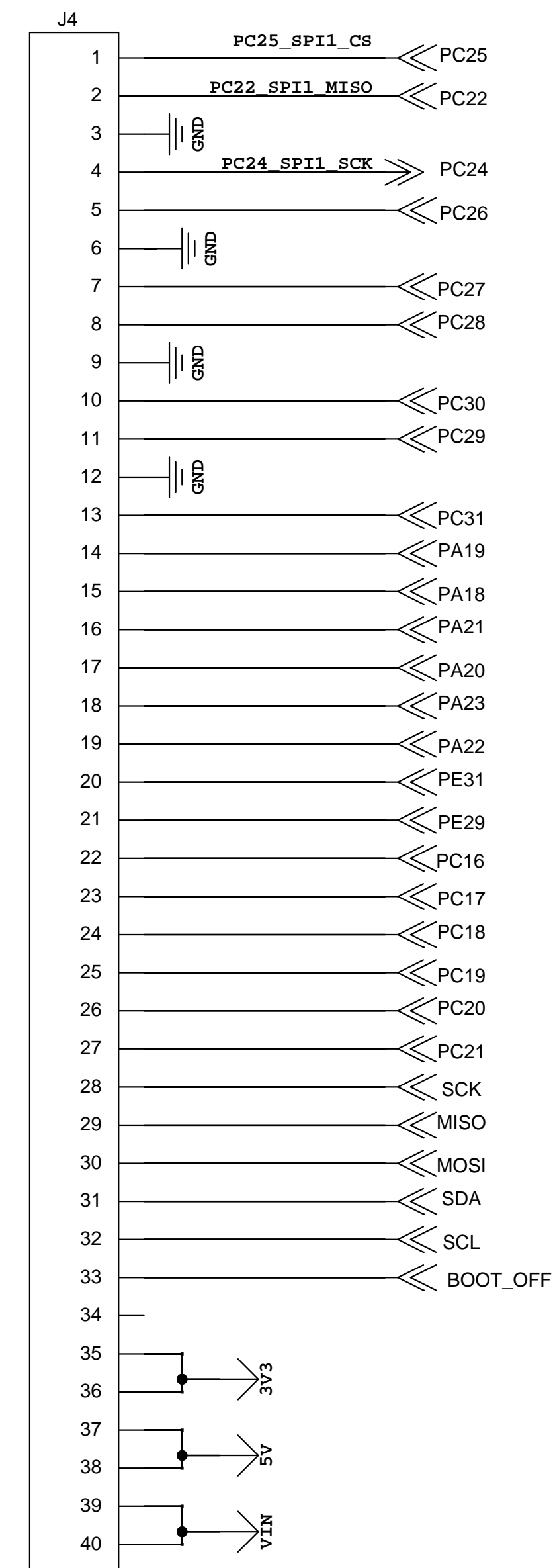
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|  <b>INFN - Sezione di PADOVA</b><br>Servizio Progettazione Officina Elettronica |  | PROJECT BY  | L. Castellani    | EXPERIMENT | QUAX | BOARD NAME | QUAX V3 CPU |
|  |  | DRAWN BY    | L. Castellani    | PAGE TITLE | CPU  | REV        | 0           |
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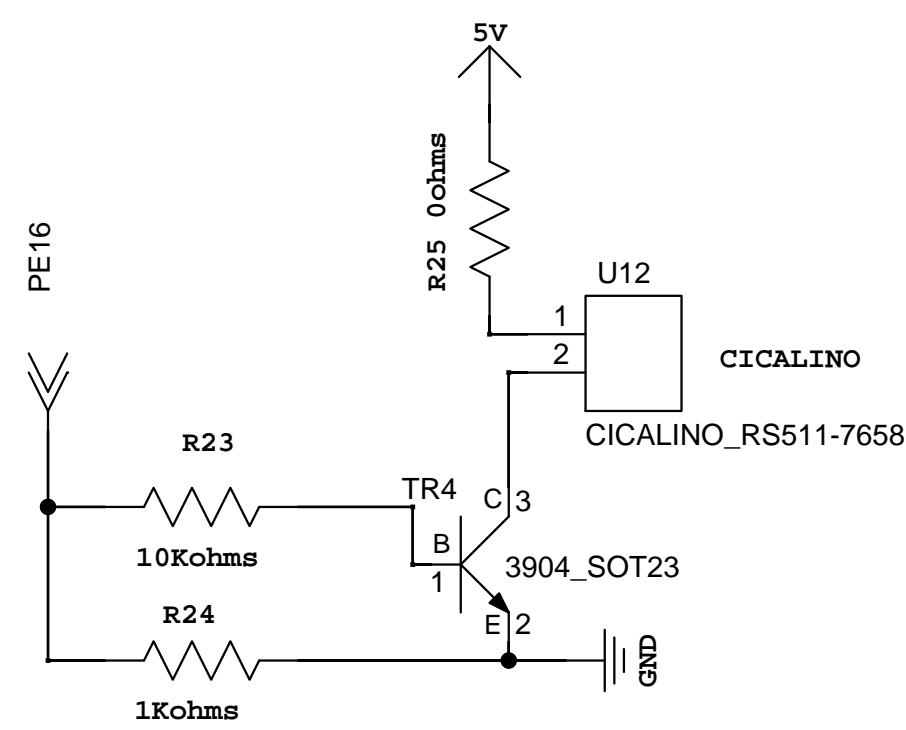
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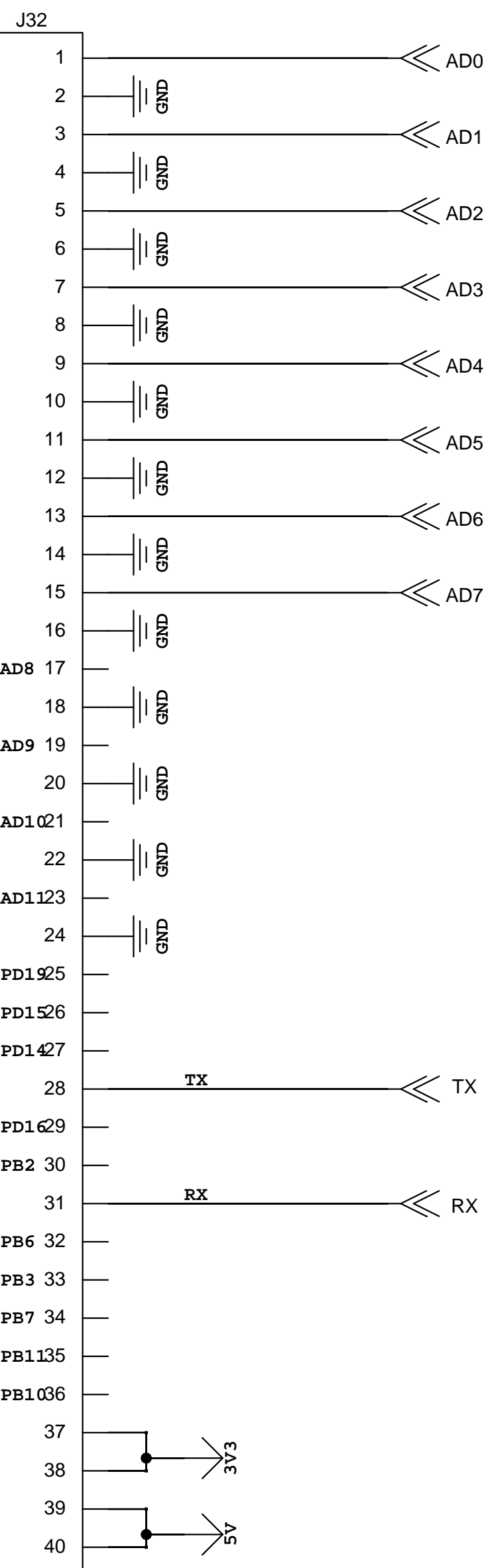
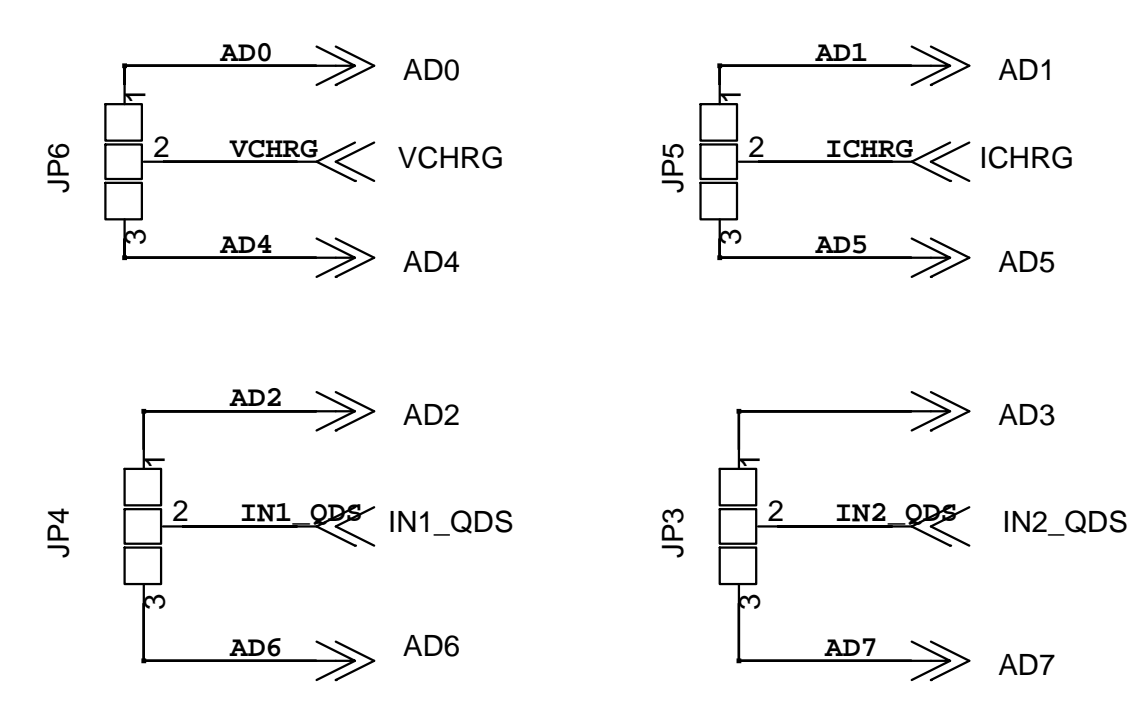


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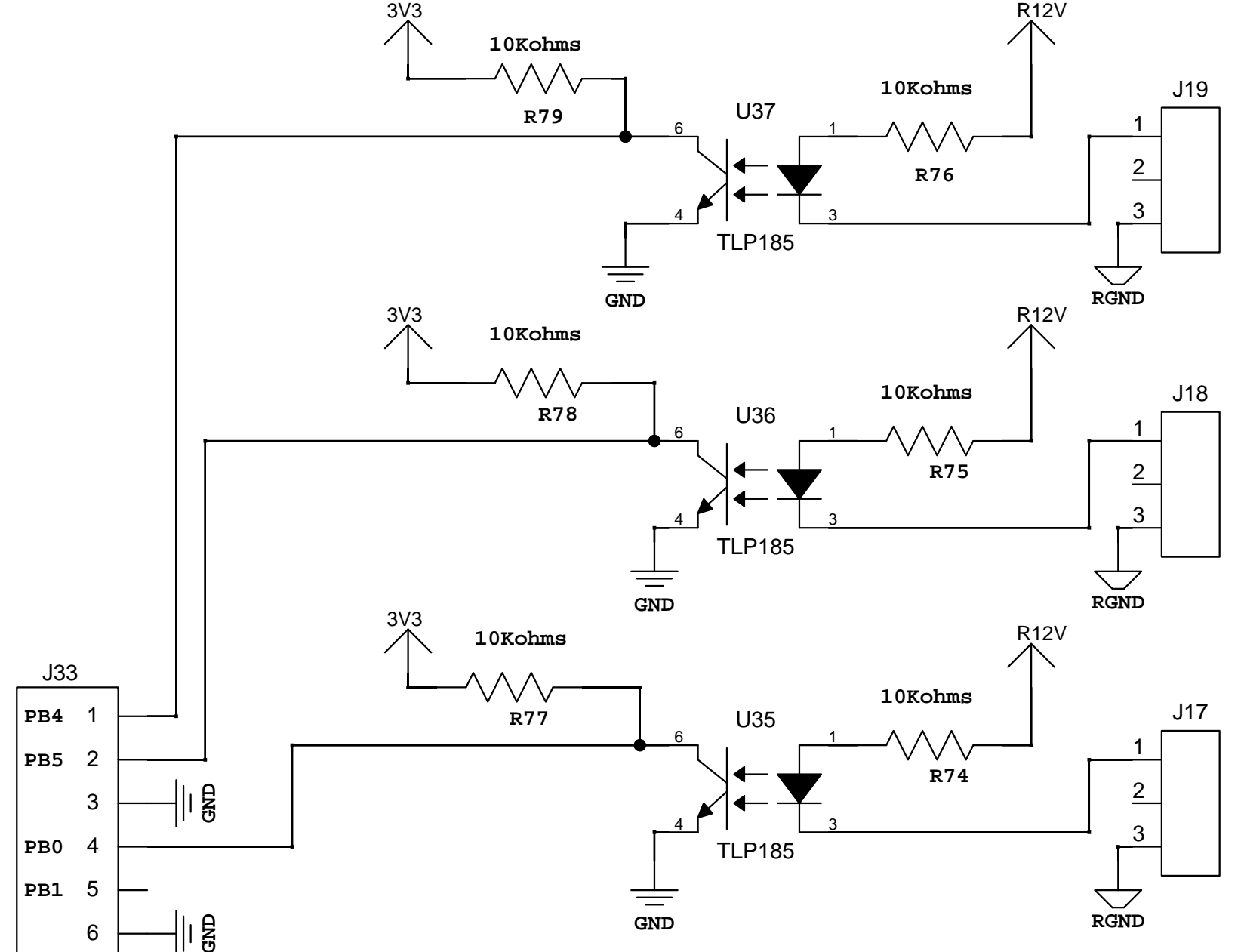


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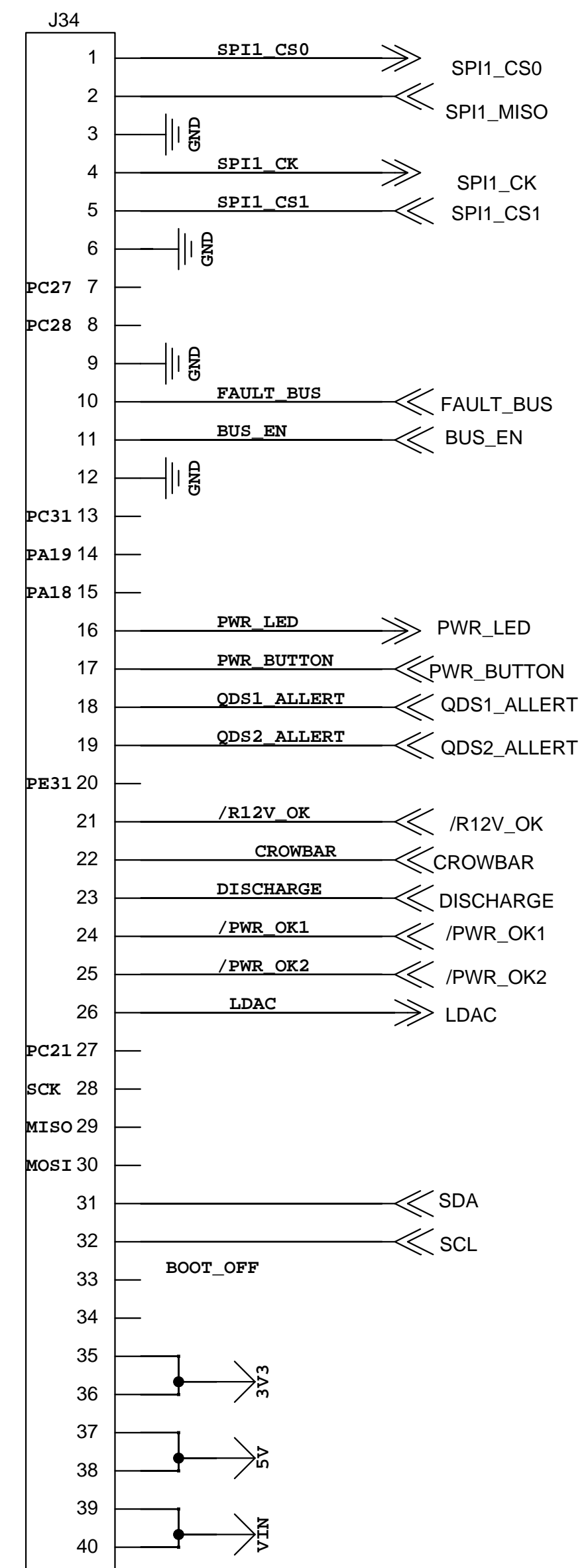




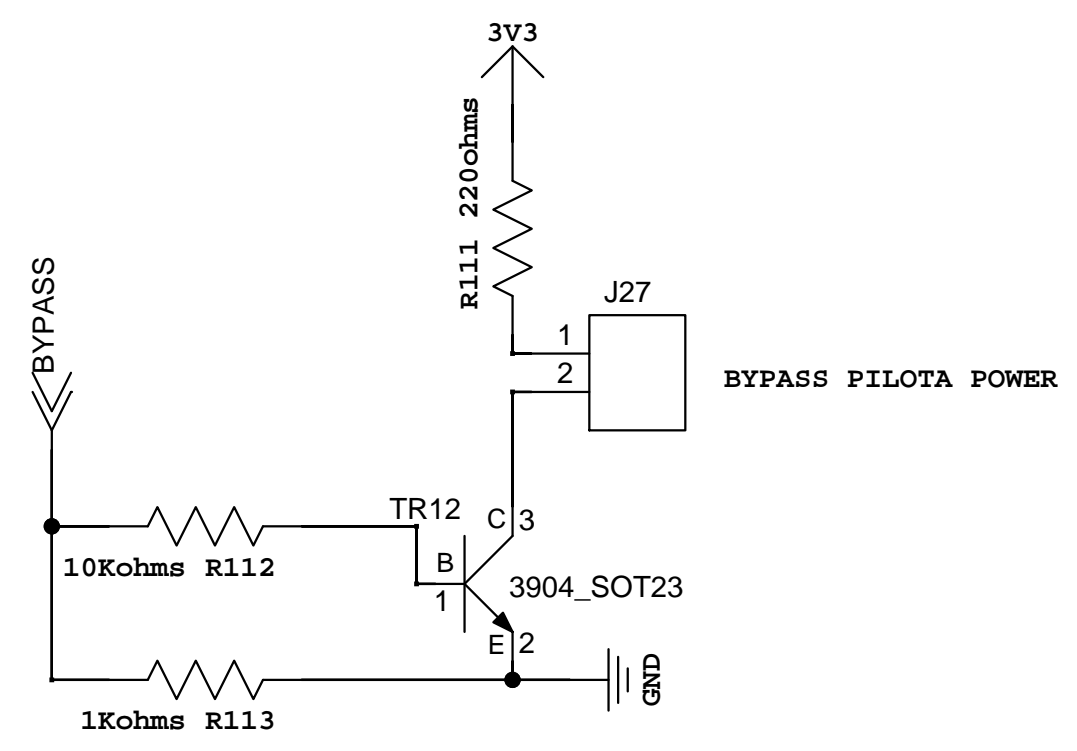
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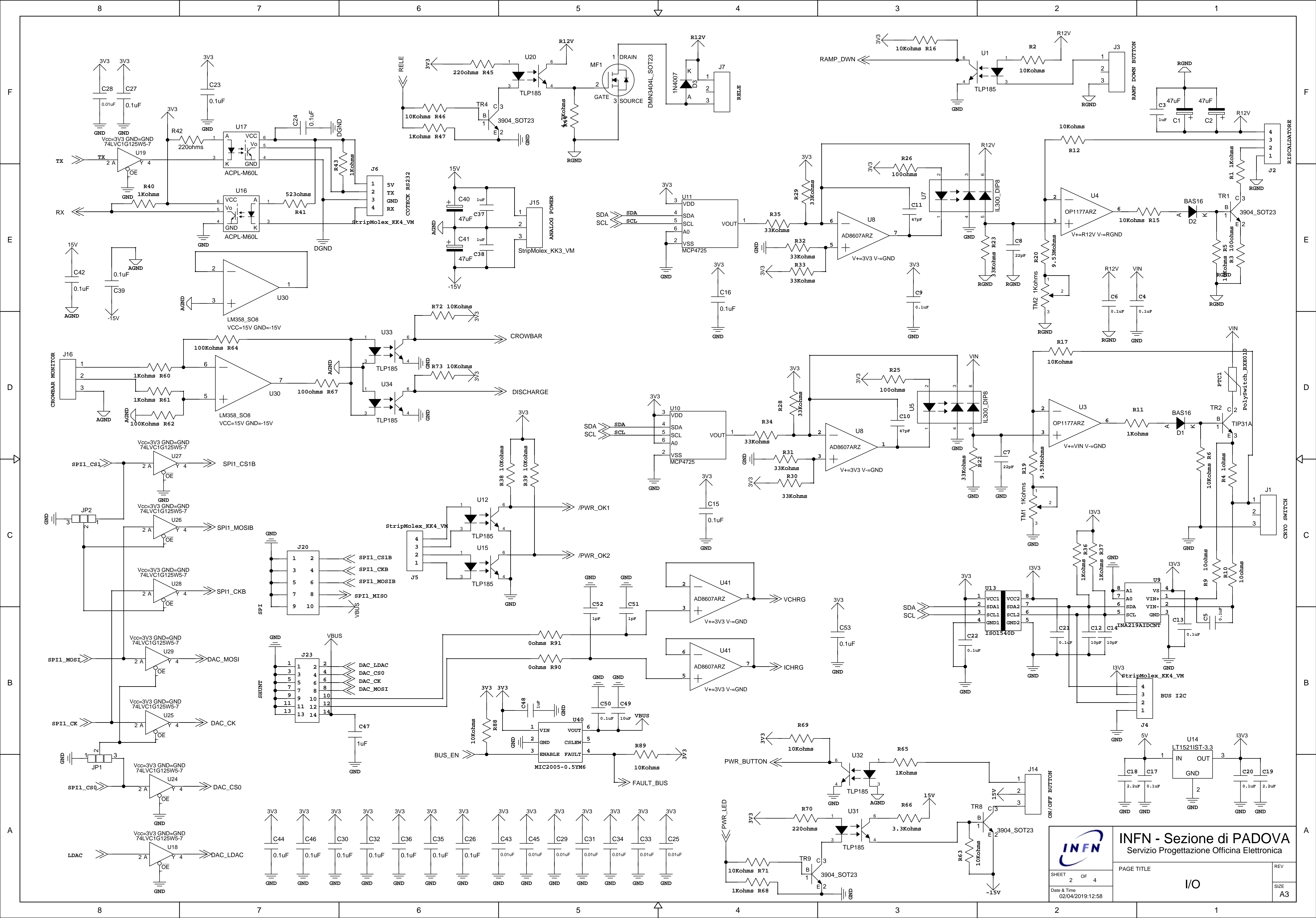
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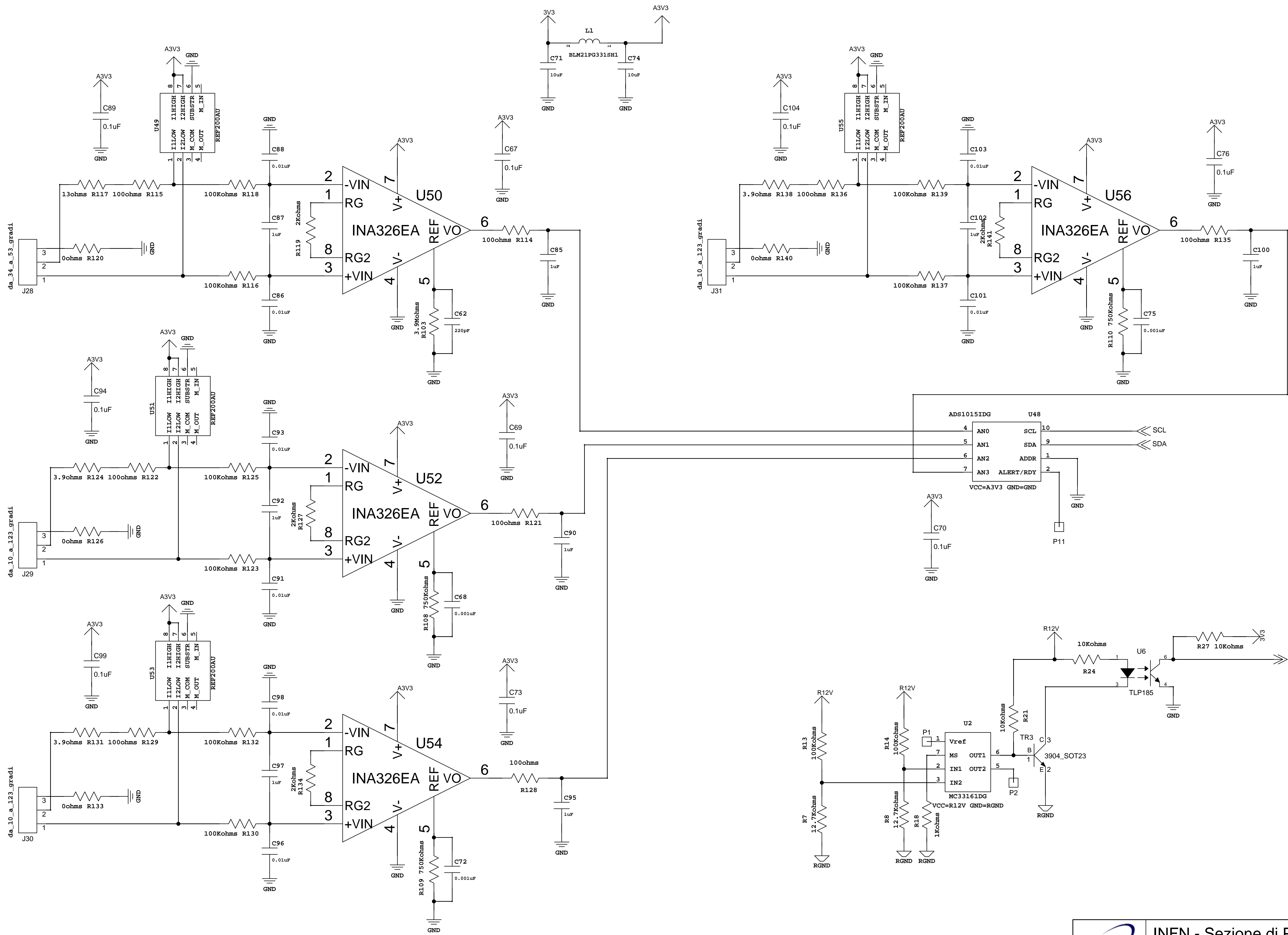


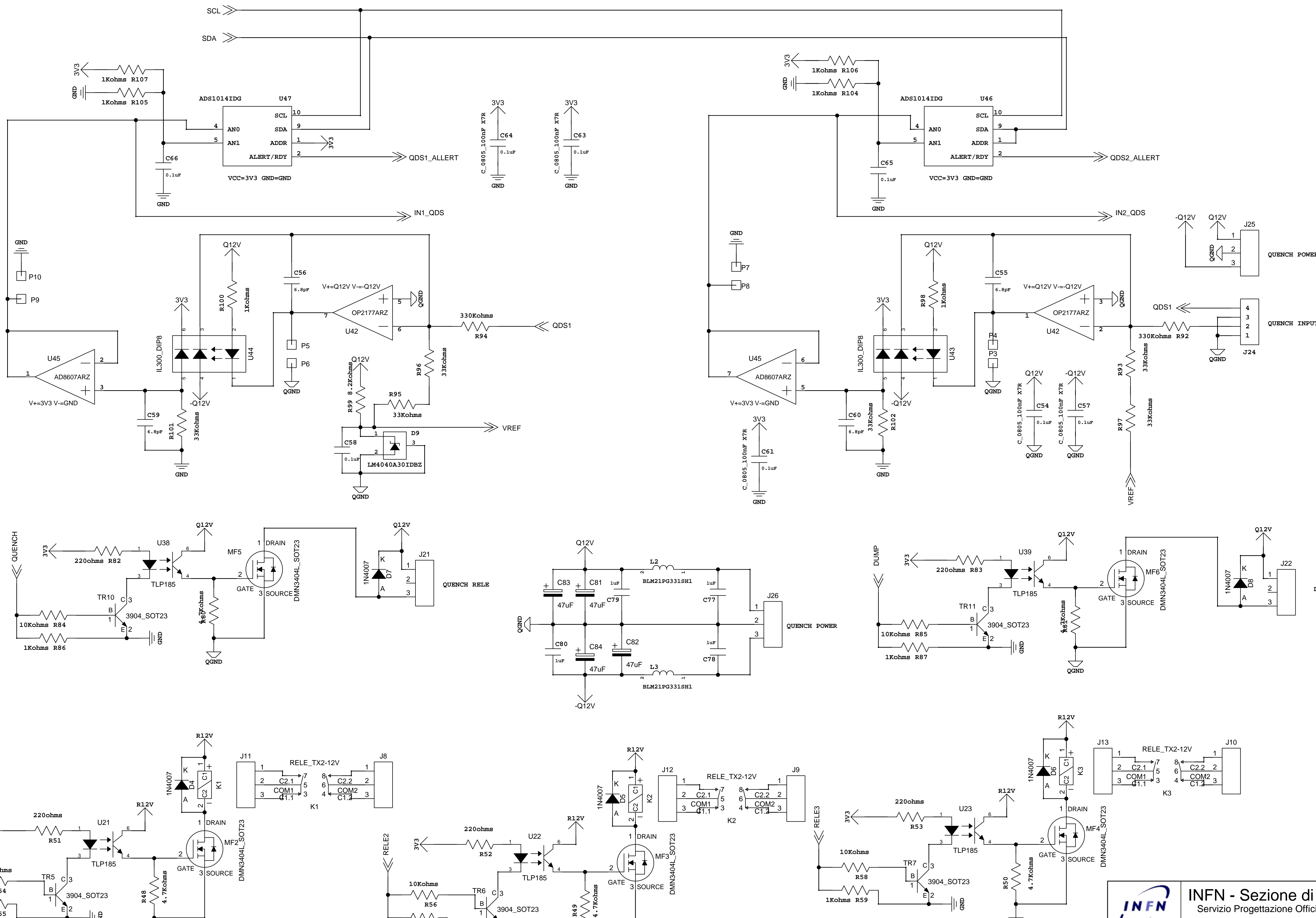
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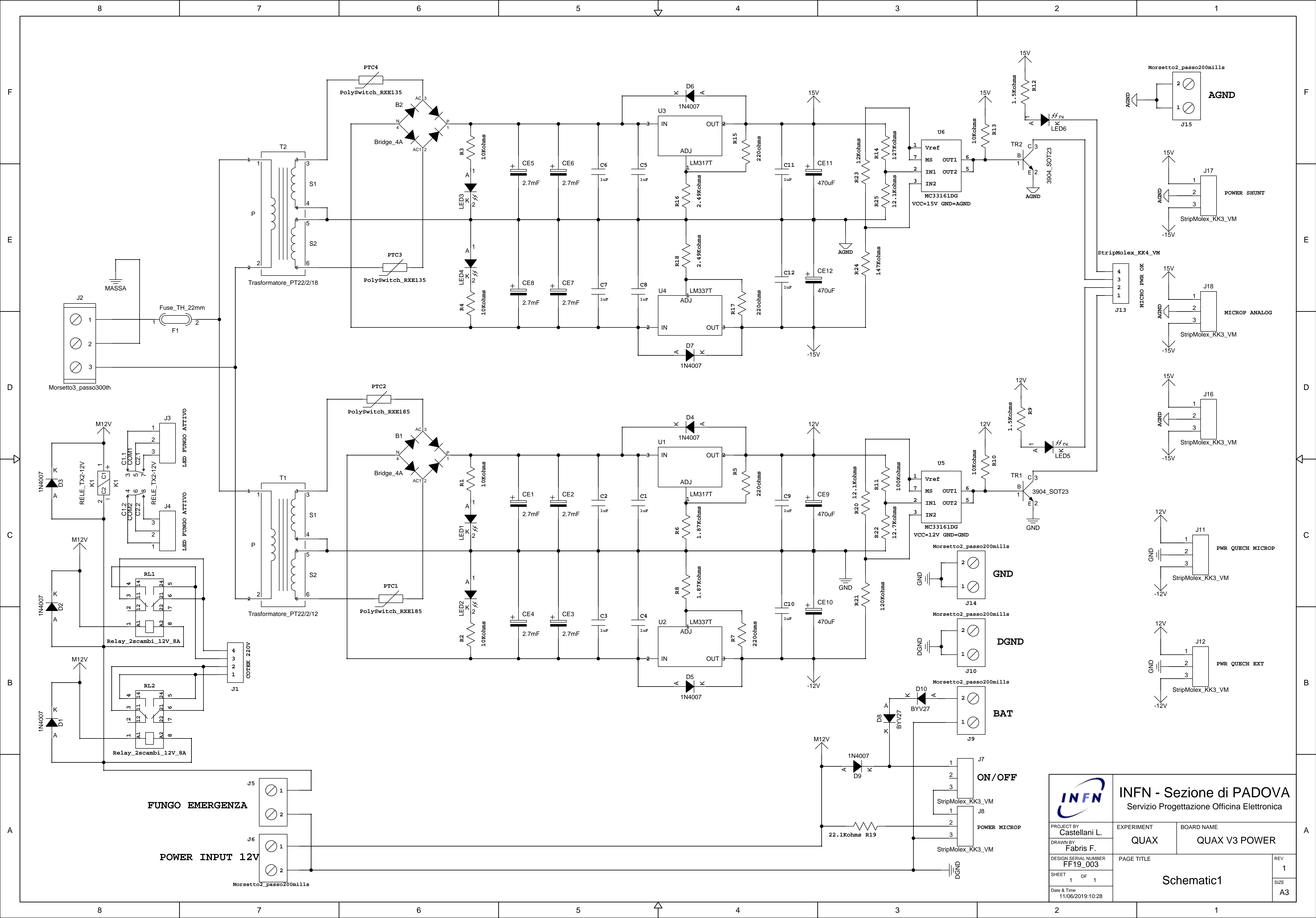


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|   |  | <b>INFN - Sezione di PADOVA</b><br>Servizio Progettazione Officina Elettronica |                    |
|   |  | PROJECT BY<br>L. Castellani  | EXPERIMENT<br>QUAX |
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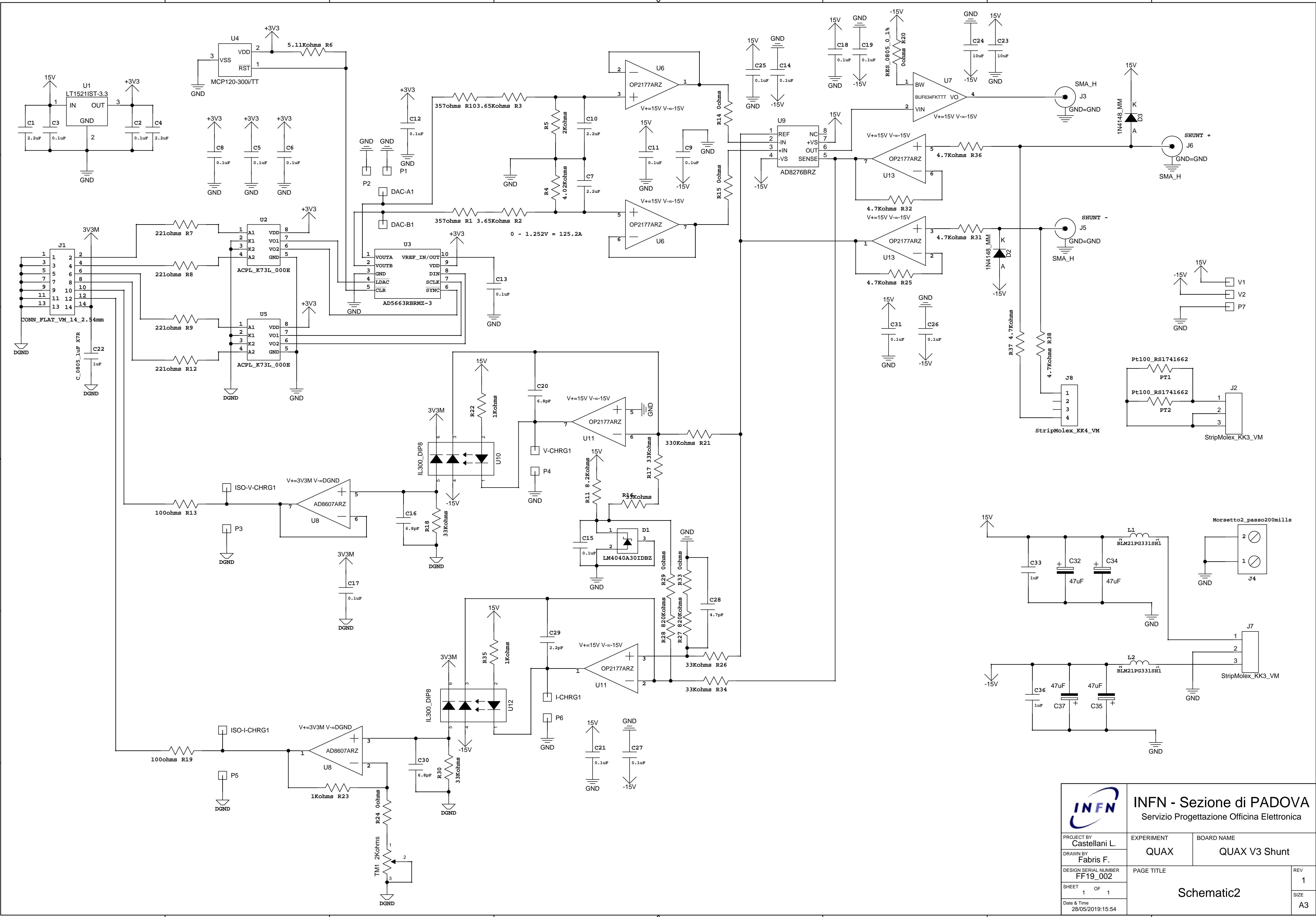






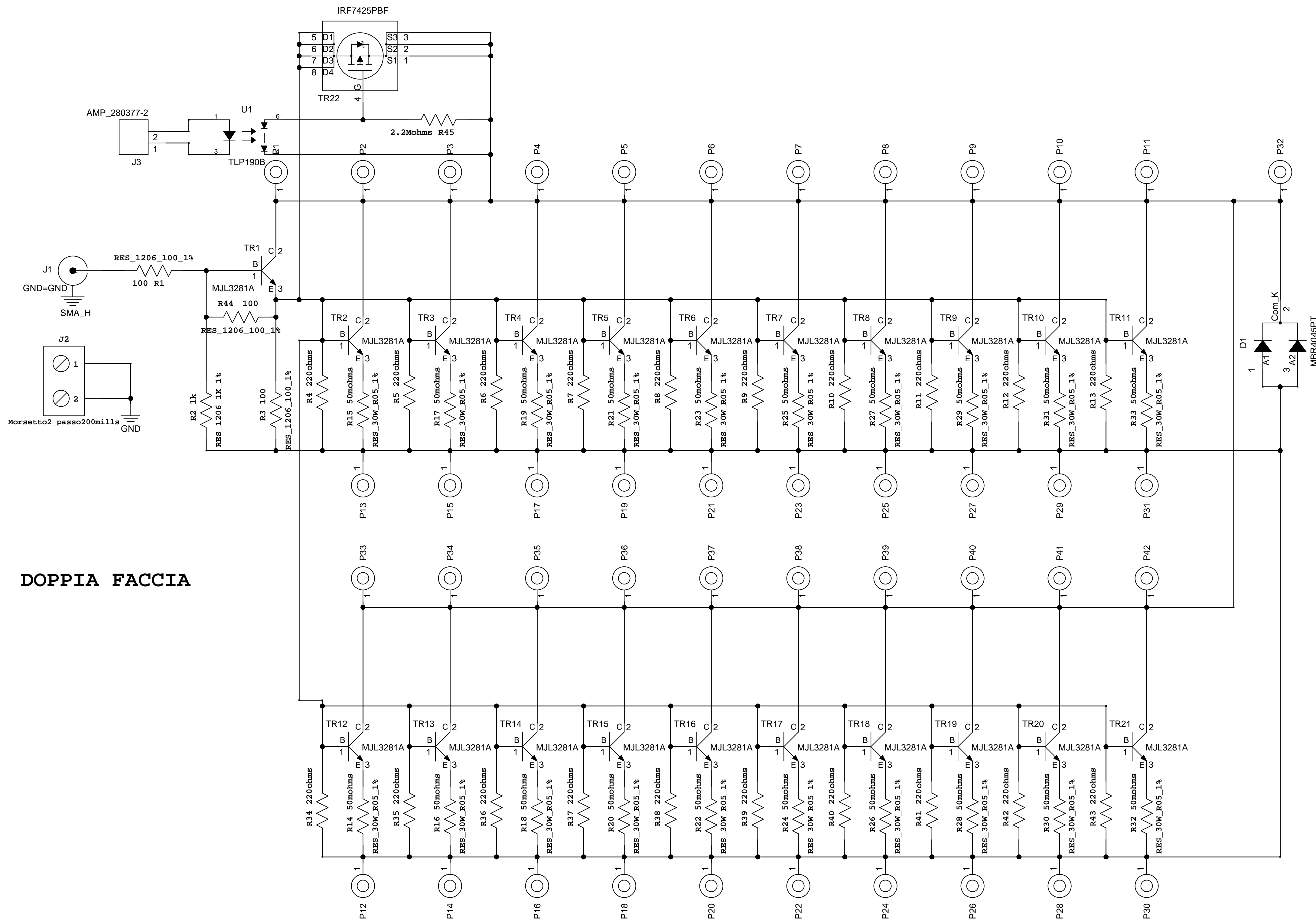


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|  <b>INFN - Sezione di PADOVA</b><br>Servizio Progettazione Officina Elettronica |  | PROJECT BY    |               |
|  |  | Castellani L. |               |
| DRAWN BY   |  | EXPERIMENT    | BOARD NAME    |
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|  <b>INFN - Sezione di PADOVA</b><br>Servizio Progettazione Officina Elettronica |  | PROJECT BY<br><b>Castellani L.</b> |  | EXPERIMENT  | BOARD NAME           |
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DOPPIA FACCIA

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|  <b>INFN - Sezione di PADOVA</b><br>Servizio Progettazione Officina Elettronica |  | PROJECT BY<br><PROJECT BY>   |  | EXPERIMENT | BOARD NAME         |
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