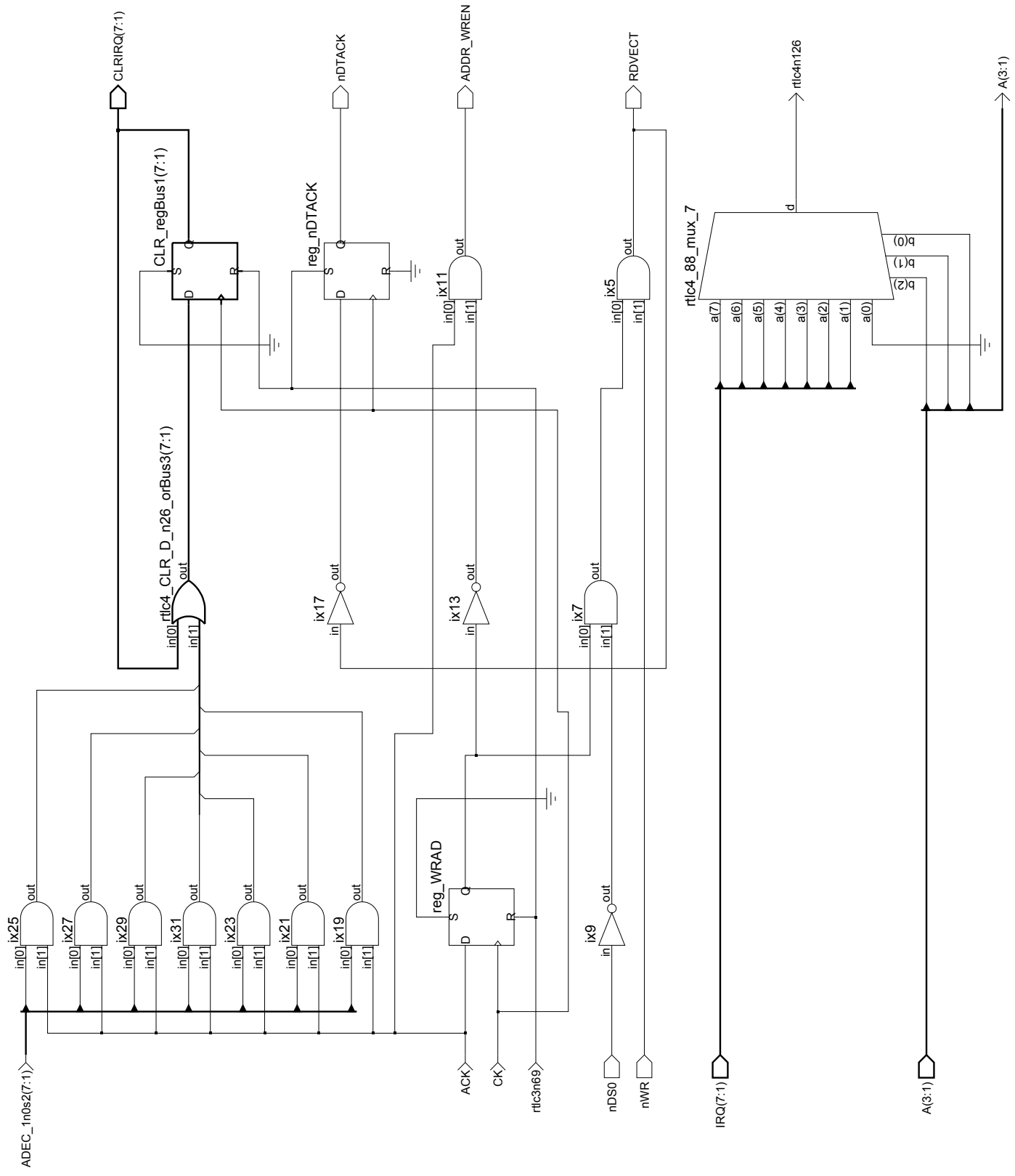
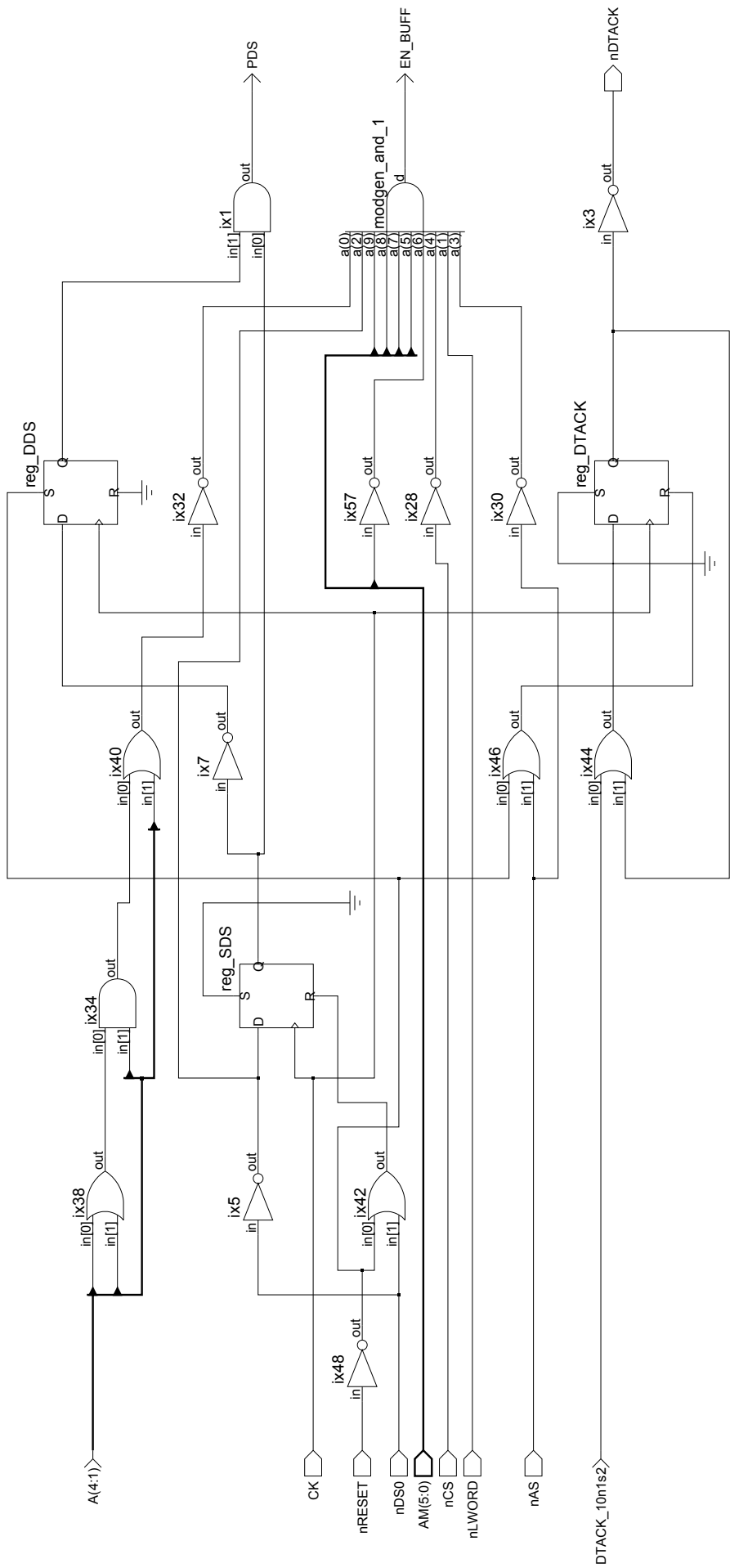
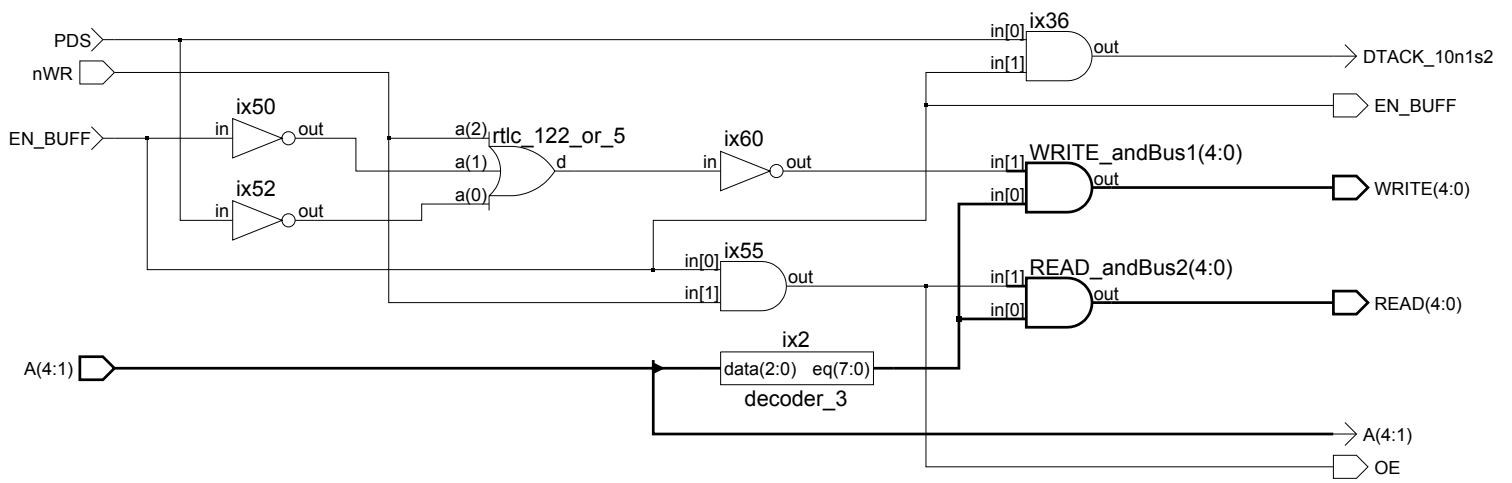


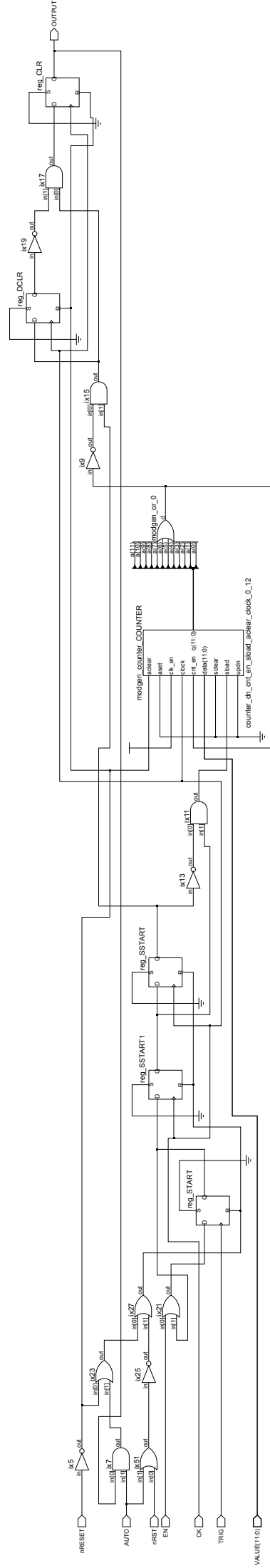
VME INTERRUPT



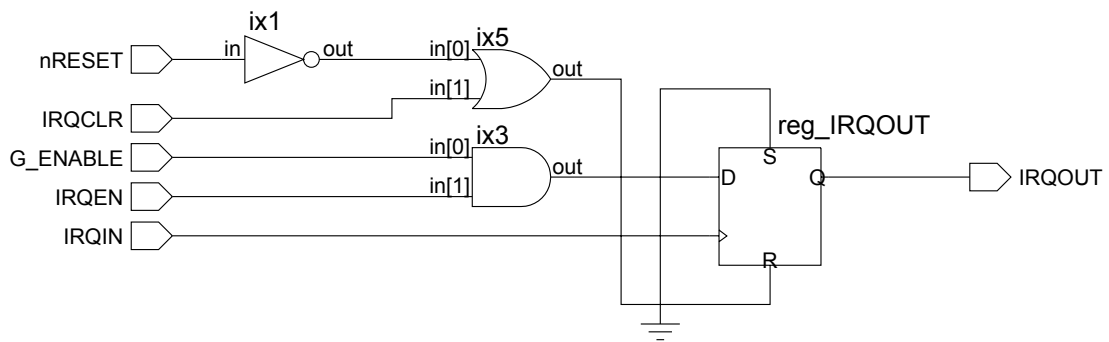
DEC IACK



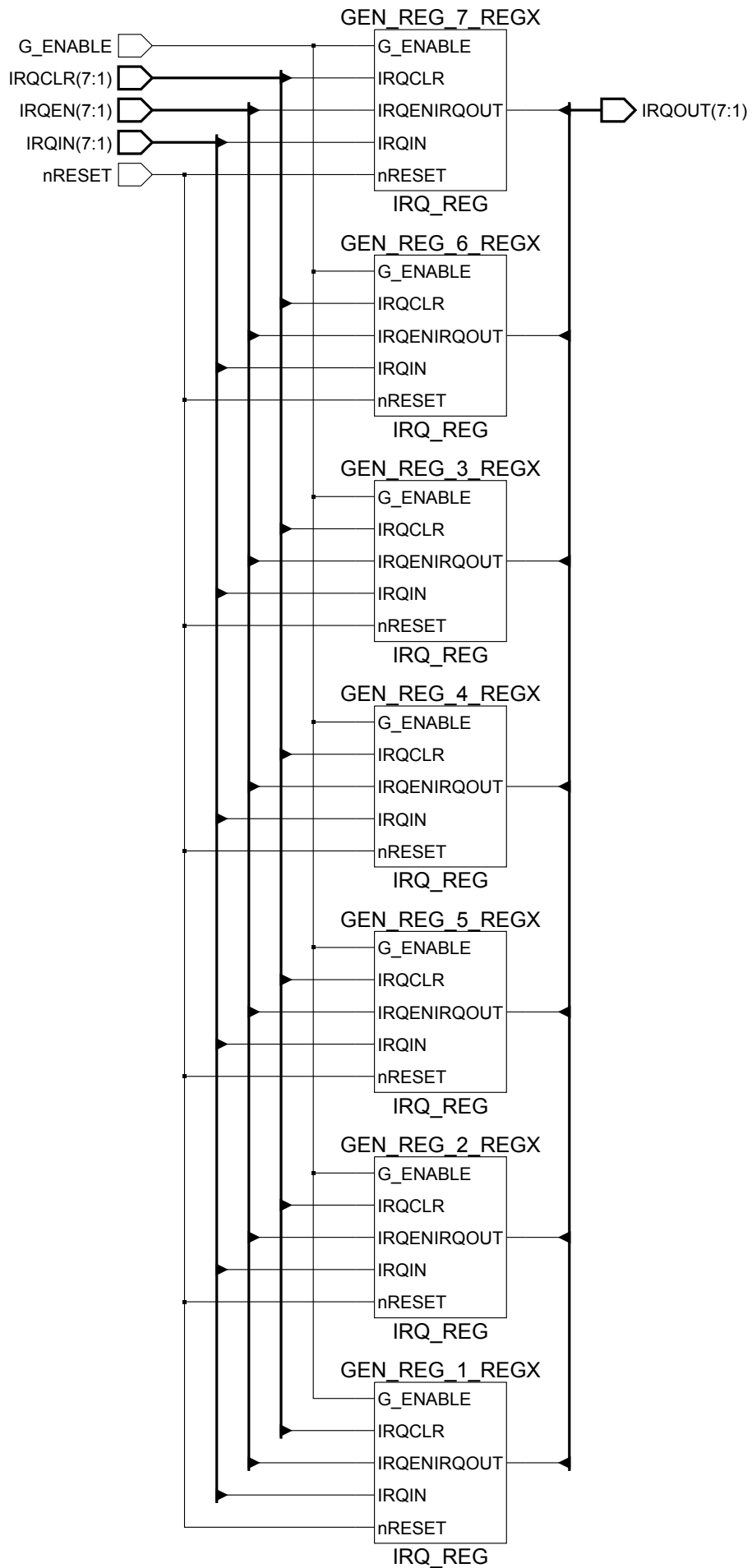


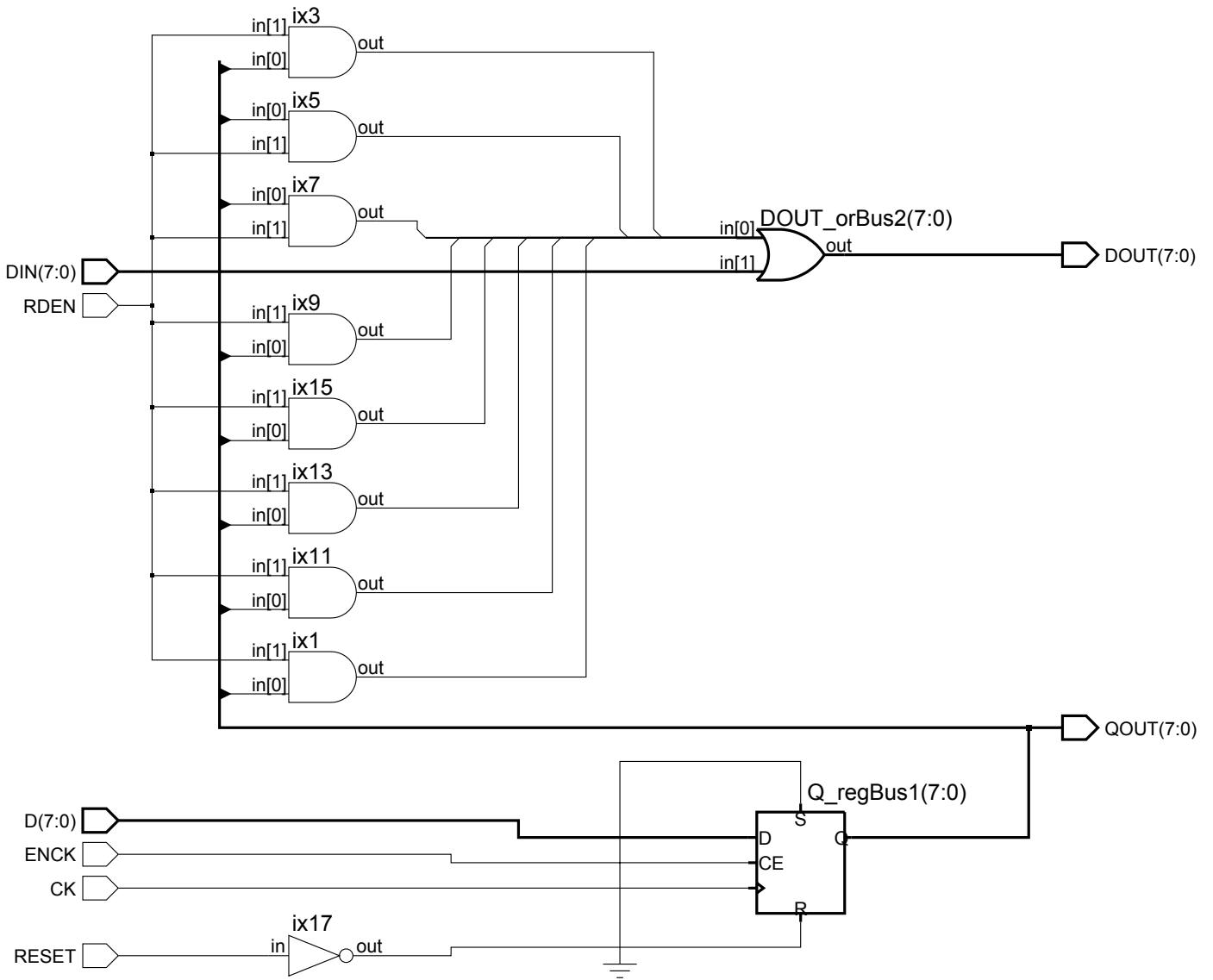


DELAY

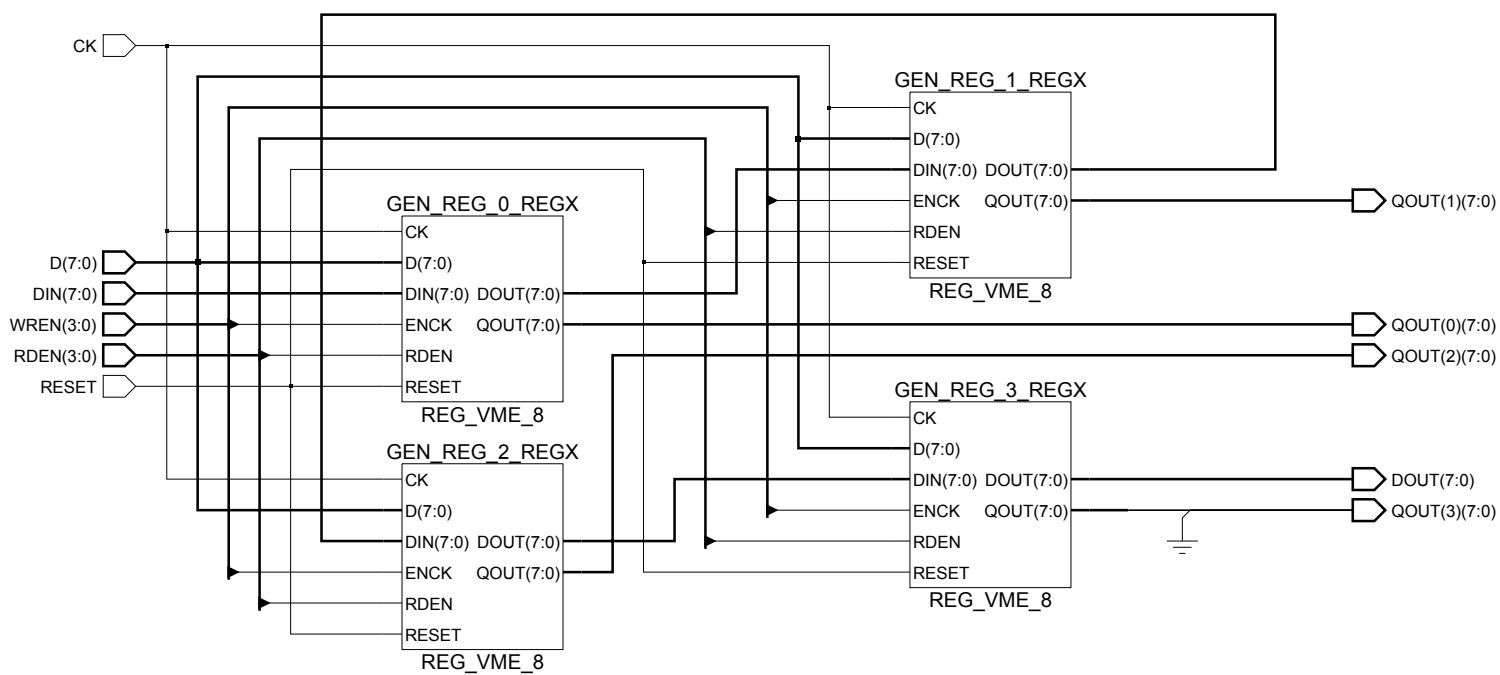


IRQ REG

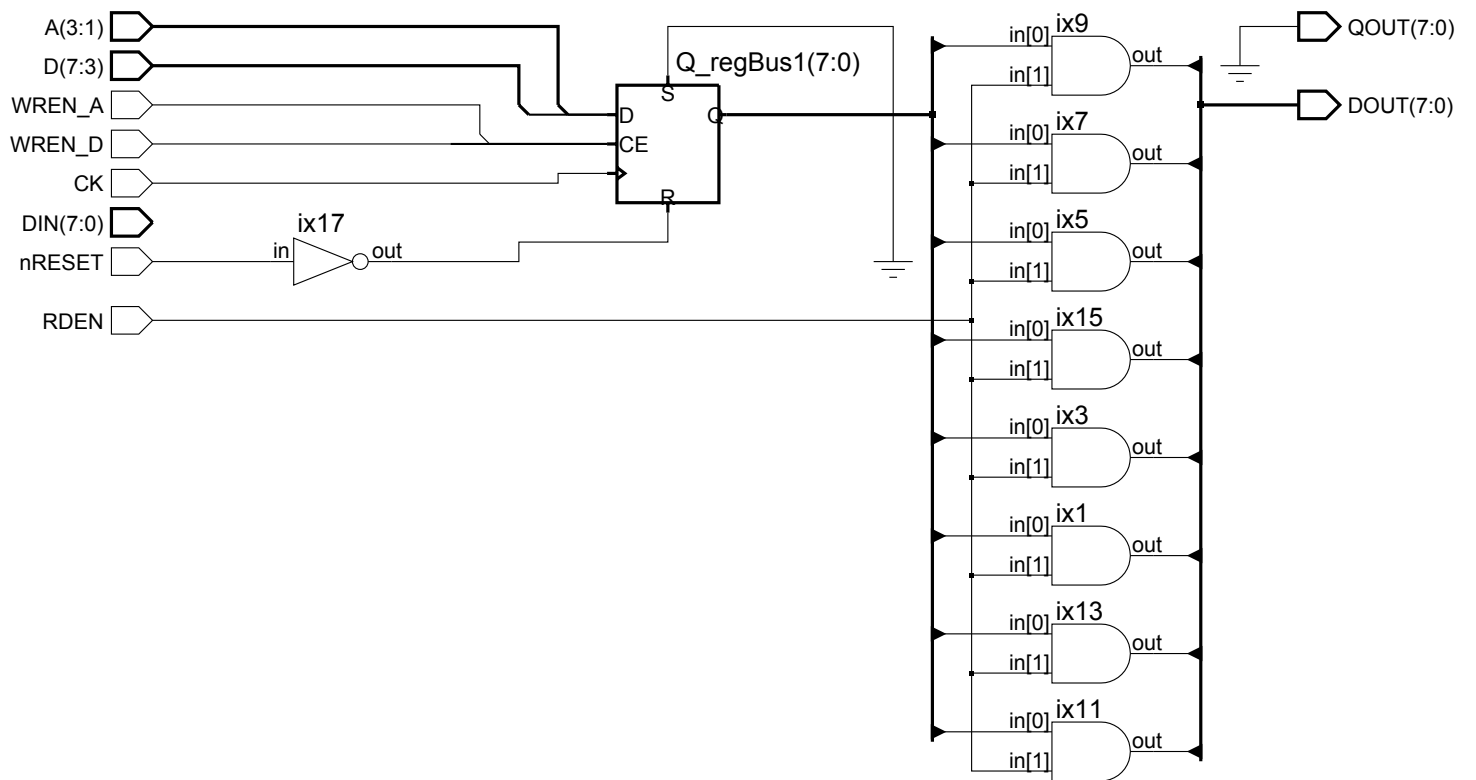




REG VME



REGS VME



VECT REG

info_registriVME.txt

BASE+0 (8bit) vettore di interrupt

BASE+2 (8bit) delay Low

BASE+4 (4bit 0..3) delay high, (1bit 4) manual clr delay, (1bit 5) auto restart delay, (1bit 6) enable delay, (1bit 7) trigsel

BASE+6 (8bit) Enable IRQ bit0=global enable, bit1=IRQ1 ...

BASE+8 (1bit 0)selTP1mux, (1bit 1)selTP2mux, (1bit 2)TP1out, (1bit 3)TP2out

selTP1mux seleziona come uscita o quella del minirate o il valore impostato su TP1out

selTP2mux seleziona come uscita o quella del minirate o il valore impostato su TP2out