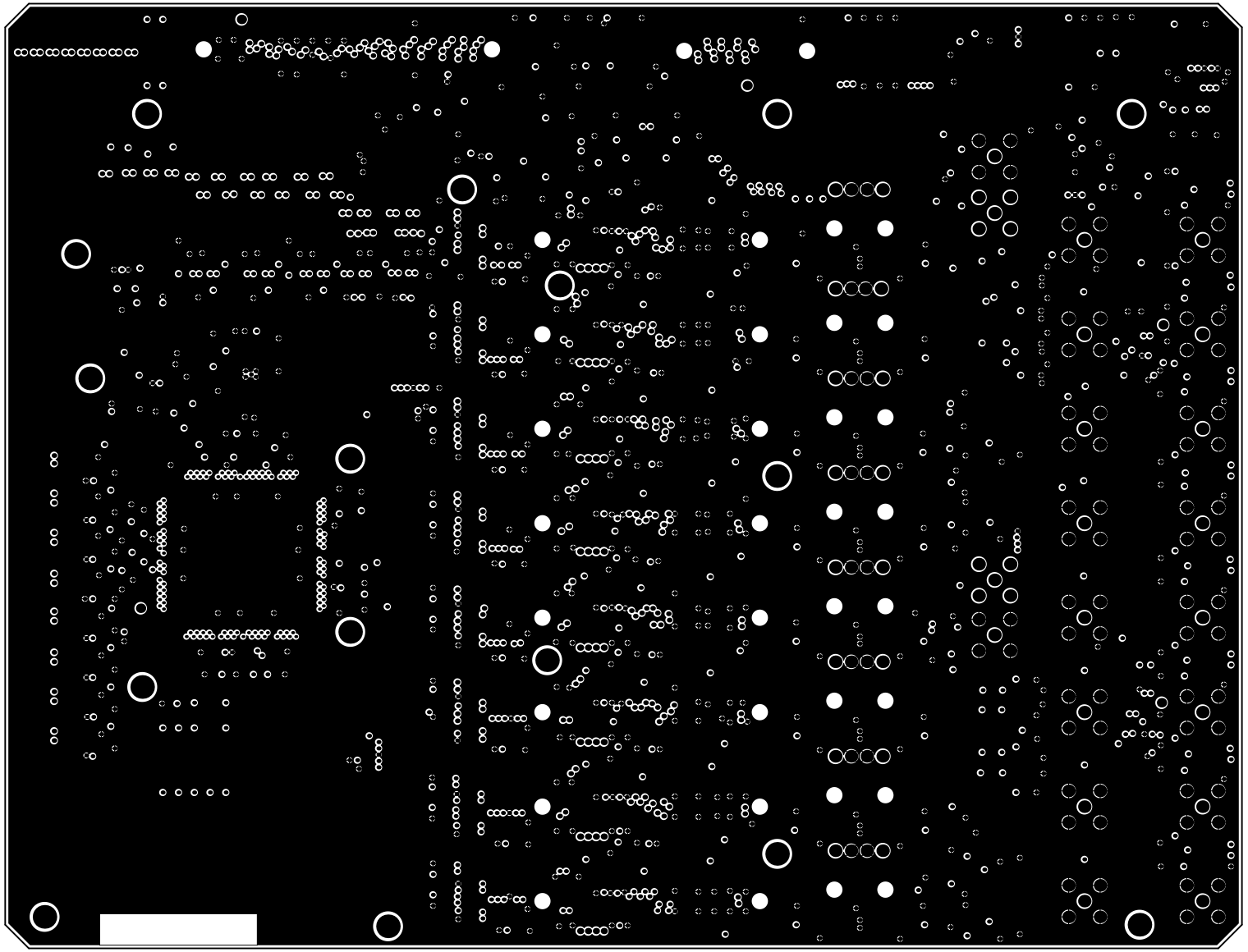
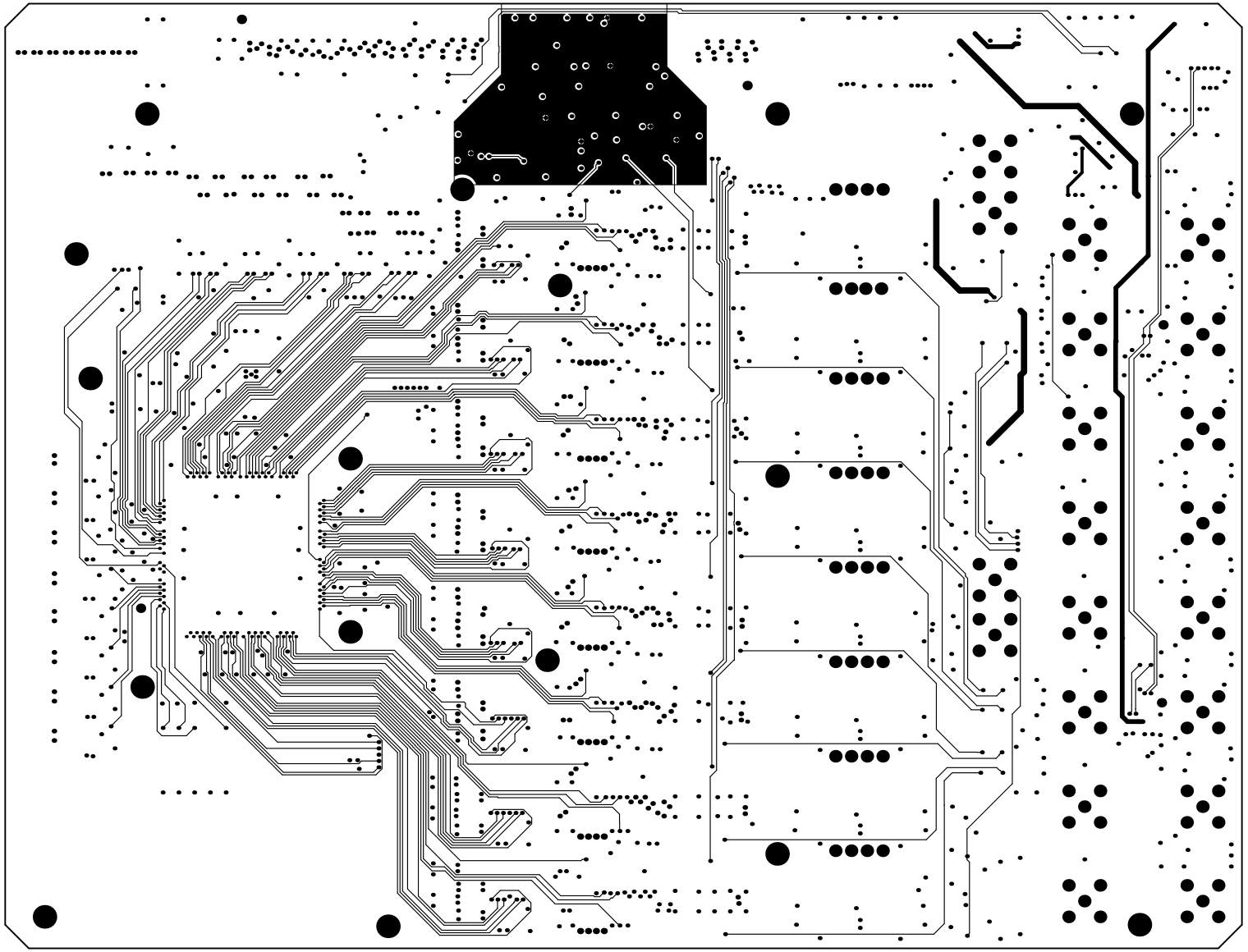
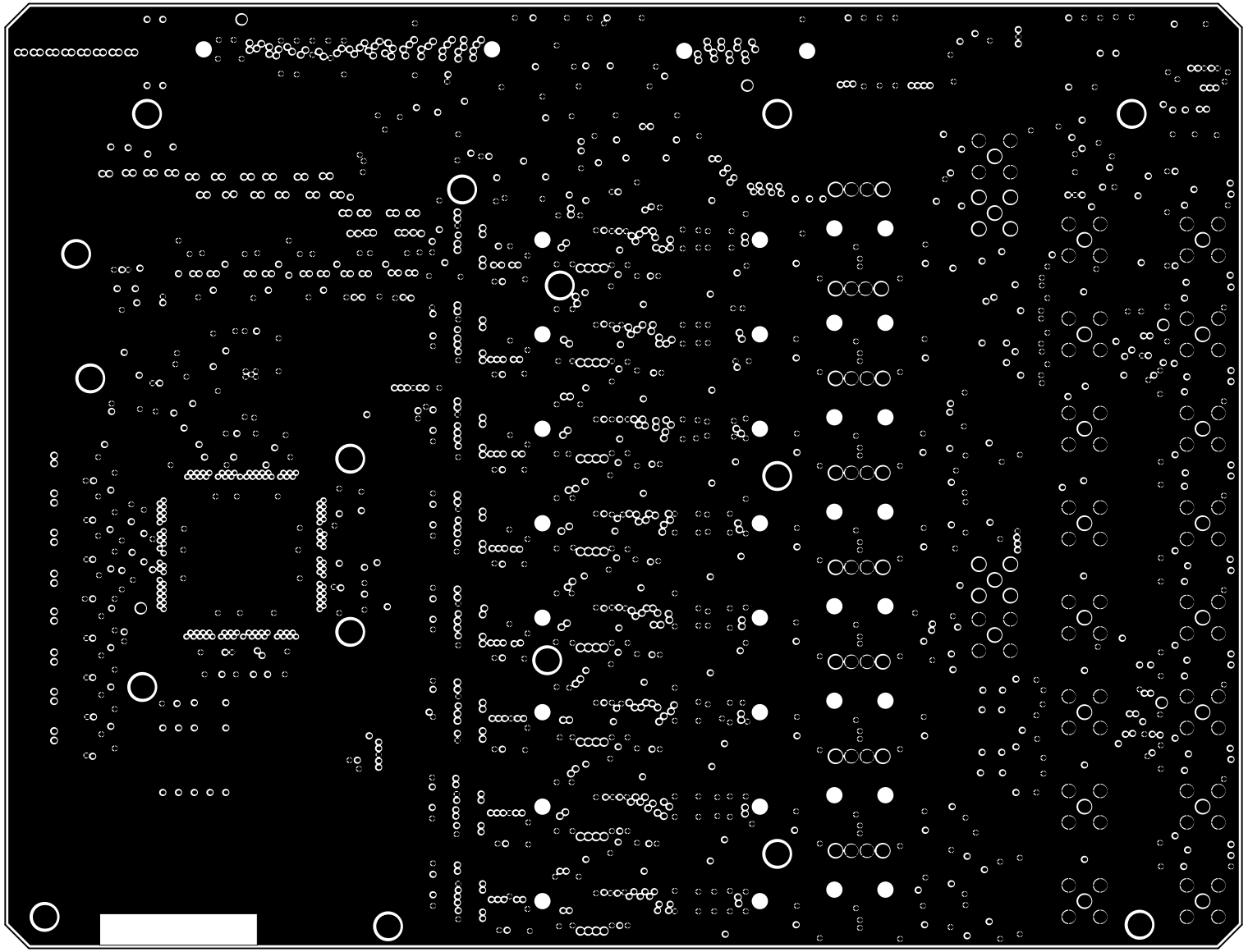
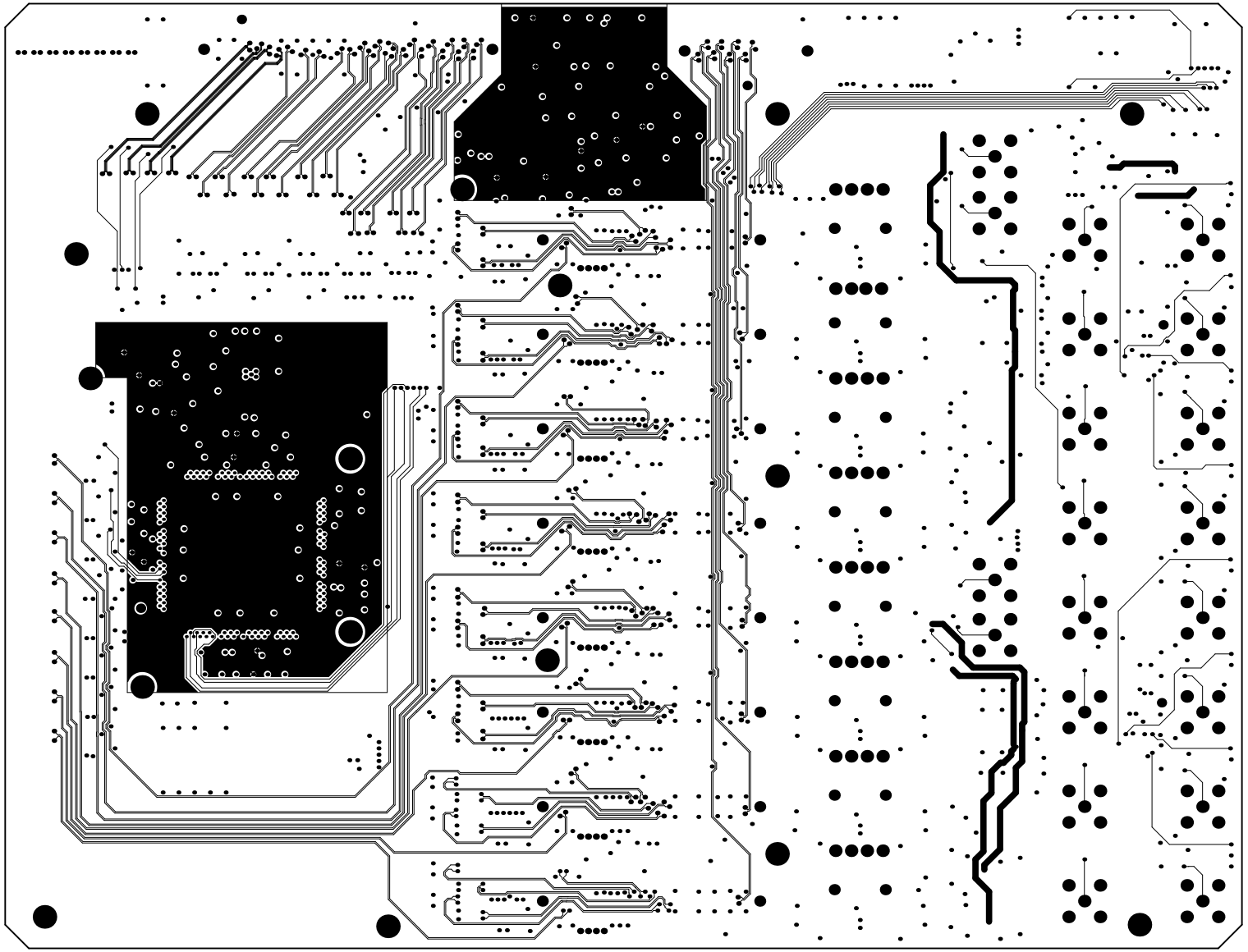
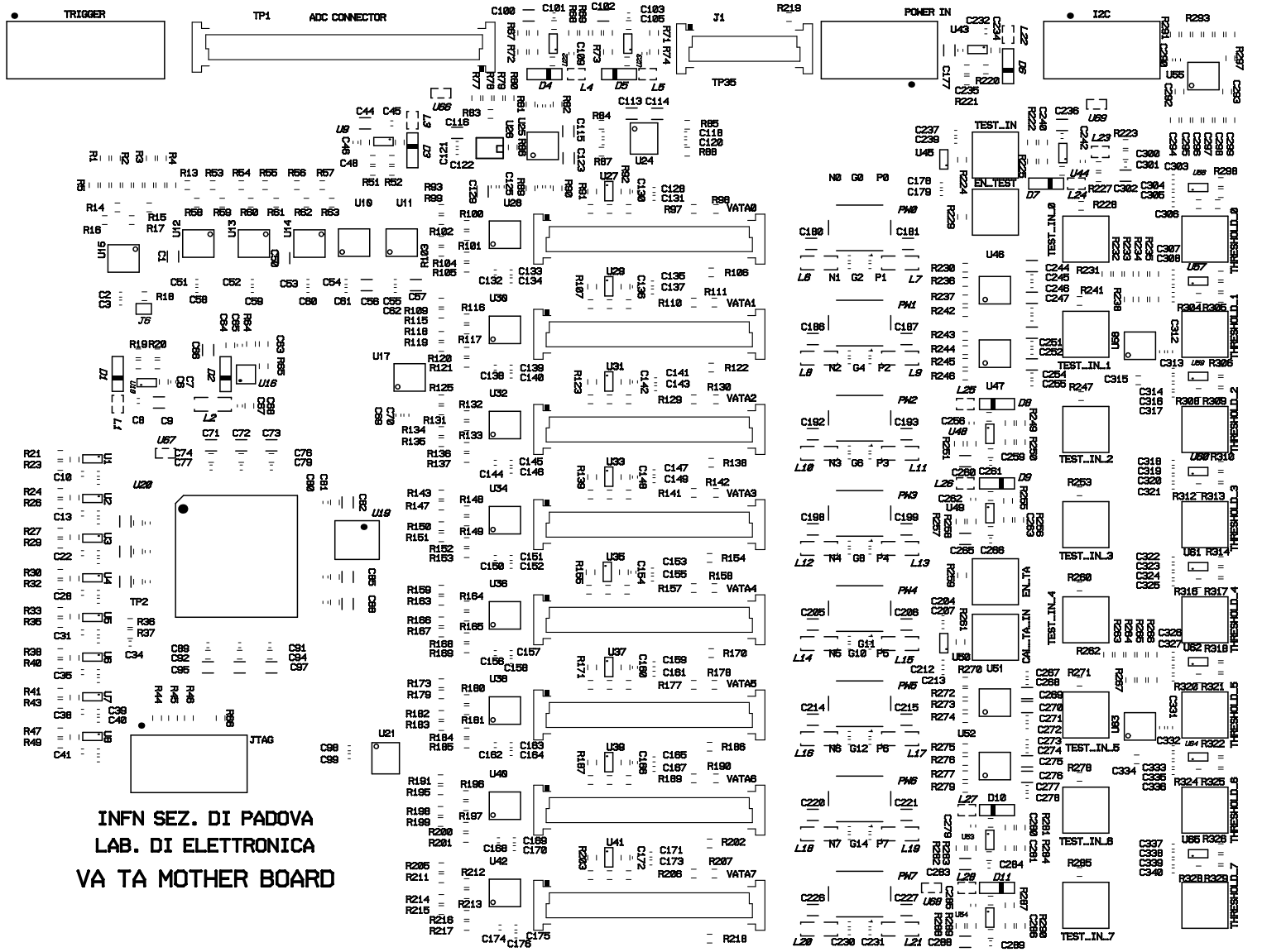






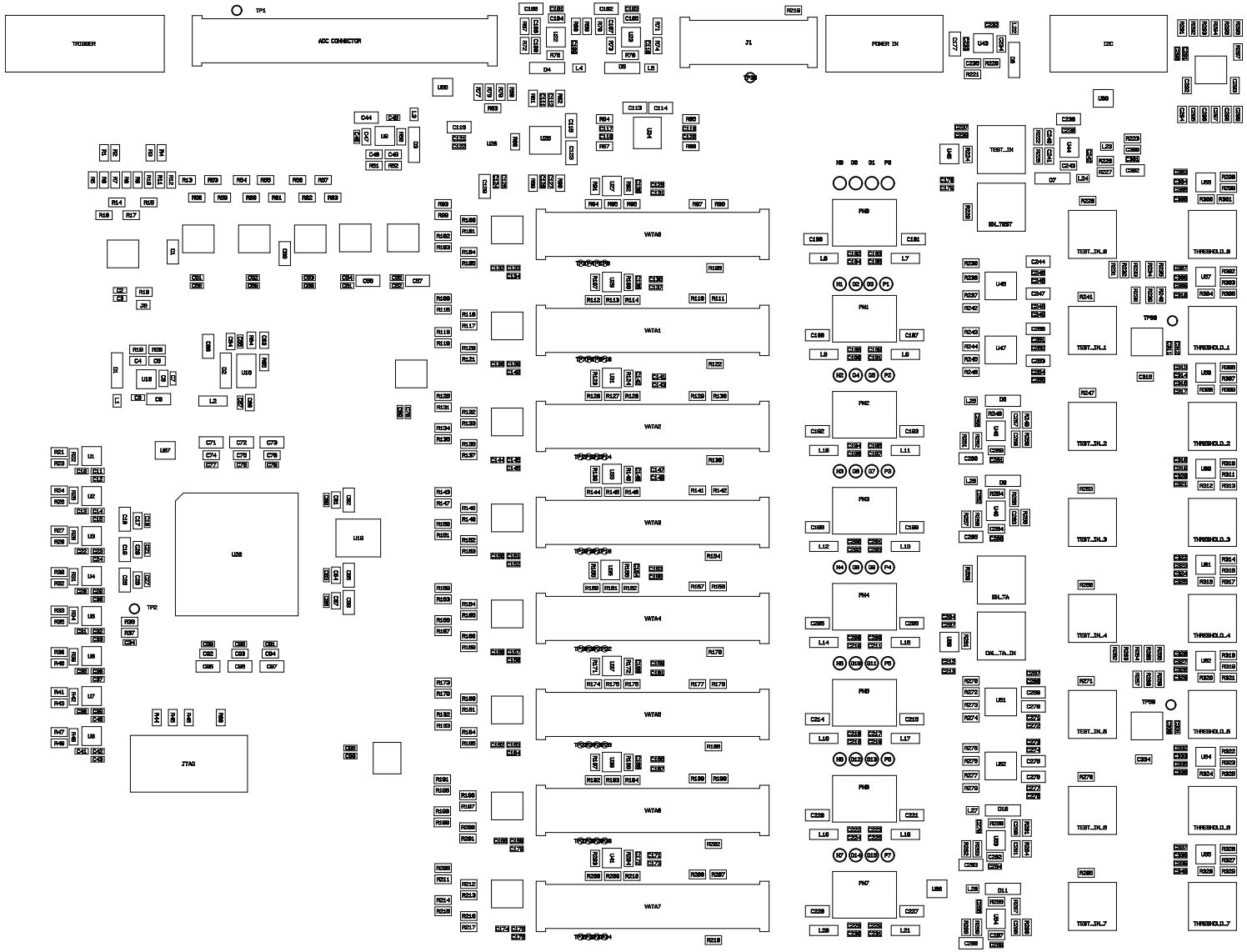




INFN SEZ. DI PADOVA

LAB. DI ELETTRONICA

VA TA MOTHER BOARD


TRIMMER
ADC CONNECTOR
J1
POWER IN
I2C
JTAG
WATA0
WATA1
WATA2
WATA3
WATA4
WATA5
WATA6
WATA7
PM0
PM1
PM2
PM3
PM4
PM5
PM6
PM7
TEST_IN
EN_TEST
EN_TA
EN_TA_IN
TEST_IN_0
TEST_IN_1
TEST_IN_2
TEST_IN_3
TEST_IN_4
TEST_IN_5
TEST_IN_6
TEST_IN_7
THRESHOLD_0
THRESHOLD_1
THRESHOLD_2
THRESHOLD_3
THRESHOLD_4
THRESHOLD_5
THRESHOLD_6
THRESHOLD_7